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7/9/03
S/N 09/955,270

#8/D
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Christophe J. Chevallier

Examiner: Edward Wojciechowicz

Serial No.: 09/955,270

Group Art Unit: 2816

Filed: September 18, 2001

Docket No.: 703.019US3

SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

This paper is in reply to the Office Action mailed on April 9, 2003. Please amend the above-identified patent application as follows.

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JUL 16 2003
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